# Abstracts

## Performance of a 6 to 18 GHz Frequency Translator Utilizing GaAs MMIC 5-Bit Digital Phase Shifter (1995 [MCS])

*S.R. Mazumder and C.M. Isham. "Performance of a 6 to 18 GHz Frequency Translator Utilizing GaAs MMIC 5-Bit Digital Phase Shifter (1995 [MCS])." 1995 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 95.1 (1995 [MCS]): 141-144.*

More than 22 dB of carrier and spurious suppression over 6 to 18 GHz has been achieved for a translator employing MMIC phase shifter. Trade-off of maximum allowed amplitude and phase errors versus number of bits are analyzed. For spurious levels <-29.8 dBc, the amplitude and phase errors must be <±0.83 dB and <±5.65 degrees respectively.

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